

Dr. Deependra Pandey
E-mail:deependra14@gmail.com
deependra14@yahoo.co.in

C-1/2, 633/515, Gahmarkunj
Behind Anand motors, Chinhat Lucknow
Mob : 09451397256

Experiences:

- Presently working as an Asst. Prof. & Program Leader in the Dept. of Electronics and Commn. Engineering, Amity University, Uttar Pradesh since 2009.
- Member, The Institution of Engineering and Technology(IET),UK
- Core Committee member during WASC accreditation
- Worked as a programme committee member to IEEE World Congress on Information and Communication Technology held on Dec 11-14,2011, Mumbai, India
- Organized National Conference, Technical Workshops ,Seminars, Symposia for Faculty and students
- Coordinated Front End VLSI Design training program for Engineering students.
- Conducted a FDP on VHDL in Amity University , Lucknow Campus
- 14 Years of teaching experiences at B.Tech. and M.Tech. level after completing M.Tech.
- Worked as an Asstt. Prof in the Dept. of Electronics and Commn. Engineering, Integral University, Lucknow for 4 Years.
- Worked as a lecturer in the Dept. of Electronics and Commn. Engineering, Radha Govind Engineering. College, Meerut.
- Appointed as an Observer, Practical Examiner, Evaluator and Paper setter by Various Technical University.
- Conducted various labs of B.Tech. and M.Tech. Level along with the thesis supervision.
- Worked as a member of Proctorial Board in Engineering college, Meerut

Ph.d.(Electronics):

- Ph.D. in Electronics completed in 2014 on the topic “Realization and study of high performance, low power current controlled differencing mode circuits for Analog Signal Processing” from RML Avadh University, Faizabad.

Educational Qualification:

- **M.Tech. (Electronics Design & Tech.)** from CEDTI, Gorakhpur(Under U.P. Tech. University, Lucknow) with **72.6%** of marks in the year 2003.
- **M.Sc. (Electronics)** from School of Electronics, Devi Ahilya University, Indore (M.P.) with **GGPA 8.648/10** (equivalent to **80.74%**) in the year 2001.
- **B.Sc. (10+2+3)** in Physics, Electronics and Mathematics from University of Gorakhpur, Gorakhpur (U.P.) with **73.6%** of marks in the year 1999.
- **Intermediate(12th)** from U.P. Board of Allahabad with **70%** of marks in the year 1996.
- **High School(10th)** from U.P. Board of Allahabad with **76%** of marks in the year 1994.

Honors Received:

- Delivered a talk in All India Radio, Lucknow on " Ozone Layer" on 17 May 2016.
- Delivered a talk in All India Radio, Lucknow on " Various Sources of Energy and their uses" on 14 Feb 2016.
- Delivered a talk in All India Radio, Lucknow on " LED, CFL, Incandescent bulb: A Comparative study" on 6 Sep 2015.
- Delivered an invited lecture in the FDP on MATLAB at Integral University, Lucknow on 3 Feb 2016.
- Appointed as a member of Elixir International Journal Editorial Advisory Board.
- Appointed as an Editorial Board Member In A R Research Publication & Conference World.

Paper Publications/Presentations:

2016

1. Published & Presented a paper titled," Crosstalk Analysis in Optical network", in a National Conference organized by B.R. Ambedkar Central University Lucknow on 24 February 2016.(isbn: 978-93-85304-35-4).
2. Presented a paper titled," Inband crosstalk due to PLI on optical layer in WDM/DWDM receiver" in a National Conference organized by Ambalika Institute,Lucknow on 19-20 February 2016
3. Presented a paper titled," Effect of Rise time and BER in Optical " in an International Conference organized by Amity University ,Lucknow on 20-21 January 2016.
4. Presented a paper titled, "Wireless sensor network based resource tracking system " in an International Conference organized by Amity University ,Lucknow on 20-21 January 2016.
5. Published a paper titled, " Wireless Sensor Network based Resource Tracking System for the Safety of Workers Exposed to Physical Agents while constructing or demolishing any Building " "in an International Journal in Feb -2016(ISSN: 2277 – 4106, Pg. No. 248-250)
6. Published a paper titled, " Wireless sensor network for real time monitoring of harmful emissions and thus providing safety to workers at construction sites" "in an International Journal in Feb -2016(ISSN: 2321 – 2055, Pg. No. 112-116)
7. Published a paper titled, " PLI in WDM/DWDM receiver "in an International Journal in Feb -2016(ISSN: 2321 – 2055, Pg. No. 179-184)

2015

8. Published a paper titled, " A Study On Artificial Intelligence Planning" "in an International Journal in Sep -2015(ISSN: 2319 – 1163, Pg. No. 36-38)
9. Published & Presented a paper titled," Comparative Study on Diamond Search Algorithm for Motion Estimation" in a National Conference organized Madan Mohan Malaviya University of Technology,Gorakhpur on 08-09 May 2015,(Pg. No. 45-48).
10. Published & Presented a paper titled," A Study on Neural Network Back Propagation Algorithm" in a National Conference organized Madan Mohan Malaviya University of Technology,Gorakhpur on 08-09 May 2015.(Pg. No. 43-44)
11. Presented a paper titled," A Study on Artificial Intelligence Planning" in a National Conference organized by Ambalika Institute,Lucknow on 14 February 2015.
12. Published a paper titled, " Study on Video Compression " in an International Journal in May -2015(ISSN: 2321 – 1814, Pg. No. 01-07)
13. Published a paper titled, " Performance Analysis of Block Based Motion Estimation Algorithms Using MATLAB" in an International Journal in March -2015(ISSN: 2278 – 8875, Pg. No. 1344-1347)
14. Published a paper titled, " Parametric Analysis of Diamond Search Algorithm for Fast Motion Estimation" in an International Journal in 2015(ISSN: 2277 – 128X, Pg. No. 47-50)
15. Published a paper titled, " Implementation of Novel Threshold Diamond Search(TDS) Algorithm for Fast Motion Estimation" in an International Journal in May -2015(ISSN: 2231 –5381, Pg. No. 268-274)
16. Published a paper titled, " A Study on Artificial Neural Network" in an International Journal in June -2015(ISSN: 2350 –0077, Pg. No. 1013-1015)
17. Published a paper titled, " Literature Review on Block Matching Motion Estimation Algorithms for Video Compression" in an International Journal in May -2015(ISSN: 2350 –0077, Pg. No. 2278-1323)
18. Published a paper titled, " Video Compression Using MATLAB" in an International Journal in May -2015(ISSN: 2278 –3814, Pg. No. 36-43)

2014

19. Presented a paper titled," Review on Block Matching Algorithms" in a National Conference organized by Amity University ,Lucknow on 26 September 2014.
20. Presented a paper titled," FPGA Based Spoken Word Recognition System" in a National Conference organized by Amity University ,Lucknow on 26 September 2014.
21. Presented a paper titled," Novel Approach for Discrete Fourier Transform calculation" in a National Conference organized by Amity University ,Lucknow on 26 September 2014.
22. Published a paper titled, " Communications Performance Enhancement And Analysis Of Diversity Techniques For Broadband Wireless" in an International Journal in May -2014(ISSN: 2347 –4289, Pg. No. 70-72)

23. Published a paper titled, " Performance Analysis of Turbo Coded OFDM System" in an International Journal in May -2014(ISSN: 2248-9622, Pg. No. 121-126)
24. Published a paper titled, " Simulation of Turbo Coded OFDM System " in an International Journal in May -2014 (ISSN: 2250-3021, Pg. No. 17-22)
25. Presented a paper titled," Study of Nanoparticles" in a National Conference organized by Ambalika Institute,Lucknow on 22 February 2014.

2013

26. Published a paper titled, " Study Of Relay System Model In Wireless Cooperative Communication" in an International Journal in April -2013 (ISSN: 2278-0181 Pg. No. 2158-2161)
27. Presented a paper titled," A Study on the Design Techniques of CMOS Analog Filters" in a National Conference organized by Ambalika Institute,Lucknow on 16 February 2013.

2012

28. Published a paper titled, " Realization & Study of High Performance MIPS RISC Processor Design Using VHDL" in an International Journal in November -2012 (ISSN: 2249-6149 Pg. No. 134-139)
29. Presented a paper titled, " Speech recognition in television system" in UGC sponsored Workshop in DDU Gorakhpur University on 22 Feb 2012.

2010

30. Published a paper titled, " Realization and Study of High Performance Voltage Mode Oscillator based on CCCCTA: A Building Block for Analog Signal Processing " "in an International Journal in Dec -2010 (issn no.: 1947-5500, Pg. No.132-138)
31. Presented a paper titled "Design And Study Of High Performance Novel Bipolar CCCDTA With Tunable Transconductance Amplifier" in International conference sponsored by IEEE in Coimbatore on 6th-7th August 10.
32. Presented a paper titled "Realization and Study of CCCCTA in an International conference sponsored by IEEE in Chennai on 29th April 10.
33. Presented and Published a paper titled "Design and study of low power current mode circuits for Analog Signal processing" in the International conference jointly organized by IMT Ghaziabad and University of Newcastle, Australia, at IMT Ghaziabad, 25-26 Feb. 2010 proceeding available at Advanced Research Series by Macmillan.(isbn no.978-0230-32941-6,Pg.No.53-64)

2008

34. Presented and Published a paper titled" Implementation of DTW algorithm for Speech Recognition on FPGA " in the IEEE sponsored conference at IET Alwar in Nov-08

2007

35. Presented and Published a paper titled "FPGA Implementation of DCT for Image Compression" in the IEEE sponsored international conference at IET Alwar in Dec-07

Projects Guided:

- Guided more than 25 projects at B.Tech. level and 12 M.Tech. Projects.

Student Feedback:

- Continuous outstanding student feedback in Amity University.
- Got an Overall grade A (Outstanding) through student feedback in Integral University Lucknow .

Conferences and Workshop Attended:

- Attended a National Conference on Information Security Challenges at B.R. Ambedkar Central University Lucknow on 24 February 2016.
- Attended 2 days International Conference on Plasma Science, Technology & application at Amity University Lucknow on 20-21 January 2016.
- Attended 2 days National Conference on Challenges & Opportunities at Ambalika Institute of Management & Technology, Lucknow on 19-20 February 2016.
- Attended one week FDP on MATLAB from 1st to 7th February 2016 at Integral University, Lucknow
- Attended a National Workshop on Embedded System & Robotics at Integral University Lucknow on 22 September 2015.
- Attended 2 Days workshop conducted by Inventrom at Amity University, Lucknow on 8-9 September 2015.
- Attended a seminar on "MATLAB" at Integral University, Lucknow on 16 September 2015
- Attended 2 days Workshop on Optical Communications at Amity University Lucknow on 9,10 April 2015.
- Attended 2 days National Conference on Integrated Circuits and Signal Processing at Amity University Lucknow on 25-26 September 2014.
- Attended One day National Conference on Challenges & Opportunities at Ambalika Institute of Management & Technology, Lucknow on 14 February 2015.
- Attended One day National Conference on Challenges & Opportunities at Ambalika Institute of Management & Technology, Lucknow on 22 February 2014.
- Attended One day Workshop on Industrial Automation using PLC &

SCADA at Amity University Lucknow on 31 October 2014.

- Attended a Seminar on MATLAB & Simulink for Engineering Education conducted by MathWorks India on 09 July 2015 and 6 August 2014
- Attended One day National Conference on Challenges & Opportunities at Ambalika Institute of Management & Technology, Lucknow on 16 February 2013.
- Attended 2 days International Conference on Recent Advances in e-communication and i-Technology at Anand Instiute of Higher Technology on 29-30 April 2010.
- Attended 3 days workshop on Speech and Image processing at NIT,Silchar(Assam) on 17-19 Dec-08
- Attended 3 days Robotics workshop at Integral University, Lucknow on 12th, 13th and 20th September 2008.
- Attended a Short Term Course on Digital System Design using VHDL at NITTTR, Chandigarh from 08 Sep to 12 Sep 2008.
- Attended an 2 days Int. Conference on Recent Applications of Soft Computing in Engineering and Technology sponsored by IEEE at IET, Alwar on 22-23 Dec-07 & 08-10 Nov-08
- Attended 2 days National Workshop on Challenges in VLSI "NWCV-06" at DAIICT, Gandhinagar on 21-22 December 2006.

Projects Undertaken:

- FPGA Implementation of Dynamic Time Warping Algorithm for Speech Recognition
Duration : 6 months
Organization : **Central Electronics Engineering Research Institute (CEERI-D)**
- Microcontroller-8051 Based Token Number Display System
Duration : 6 months
Organization : **School Of Electronics, DAVV, Indore**

Industrial Training:

- PCB Designing & Electronic Switching Assembly
Duration : 3 months
Organization : **Indian Telephone Industries Ltd., Manakapur (U.P.)**

PERSONAL DETAILS

Name : Dr.Deependra Pandey
Father's Name : Sri Diwakar Pandey
Date of Birth : 10-03-1979
Sex : Male
Marital Status : Married
Nationality : Indian
Hobbies : To interact with new people, Traveling
Achievements : National Scholarship Holder
Permanent Add. : H.No.-292 E,
38 A Circular Road,
P.O. - Rly Colony Bichhiya,
Gorakhpur (U.P.).
Mob: 09451397256
E-mail : deependra14@yahoo.co.in

References:

Prof.(Dr.) V.K. Singh
HOD (ECE),
IET, Lucknow(U.P.)
Mob: 09335025611
vksingh@ietlucknow.edu

Prof.(Dr.)L.K.Singh
Ex-Director,IET
Dr.RML Avadh University
Faizabad (U.P.)
Mob:9415140090
proflksingh.iet@gmail.com